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Part Number: DMR100A

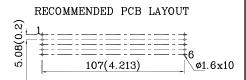
101.2mm (4.0") SINGLE DIGIT NUMERIC DIS-**PLAY**

Features

- Low power consumption
- ullet Robust package
- I.C. Compatible
- \bullet Standard configuration: Gray face w/ white
- ullet Optional black face provides superior color contrast
- RoHS Compliant







Package Schematics 59.5(2.343) 10° \$0.8(0.031)+8 101.2(3.984) 122(4.803) 107(4.213)DΡ ø10(0.394) 90(3.543) 7(0.276)±0.5 15(0.591) 3,8

1. All dimensions are in millimeters (inches), Tolerance is $\pm 0.25 (0.01")$ unless otherwise noted.

34.84(1.372)

2. Specifications are subject to change without notice.

5.08(0.2)

Absolute Maximum Ratings (T _A =25°C)		MR (GaAlAs)	Unit	
Reverse Voltage (Per Chip)	V_{R}	5	V	
Forward Current (Dp)	I_{F}	60 (30)	mA	
Forward Current (Peak) 1/10 Duty Cycle 0.1ms Pulse Width (Dp)	ifs	310 (155)	mA	
Power Dissipation (Per Chip)	P_D	150	mW	
Operating Temperature	$T_{\rm A}$	-40 ~ +85	°C	
Storage Temperature	Tstg	-40 ~ +85	-0	
Lead Solder Temperature [2mm Below Package Base]	260°C For 3-5 Seconds			

Operating Characteristics (T _A =25°C)		MR (GaAlAs)	Unit
Forward Voltage (Typ.) (I _F =10mA)(Dp)	V_{F}	7.2 (3.6)	V
Forward Voltage (Max.) (I _F =10mA)(Dp)	V_{F}	10 (5.0)	V
Reverse Current (Max.) (V _R =5V)(Per Chip)	I_{R}	10	uA
Wavelength of Peak Emission CIE127-2007* (Typ.) (I _F =10mA)	λΡ	655*	nm
Wavelength of Dominant Emission CIE127-2007* (Typ.) (I _F =10mA)	λD	640*	nm
Spectral Line Full Width At Half-Maximum (Typ.) (I _F =10mA)	$\triangle \lambda$	20	nm
Capacitance (Typ.) (V _F =0V, f=1MHz)	С	45	pF

Part Number	Emitting Color	Emitting Material	Luminous Intensity CIE127-2007* $(I_F=10\text{mA})\text{ucd}$		Wavelength CIE127-2007* nm λP	Description
			min.	typ.		
DMR100A	Red	GaAlAs	5200 14000*	119990 28990*	655*	Common Anode, Rt.Hand Decimal.

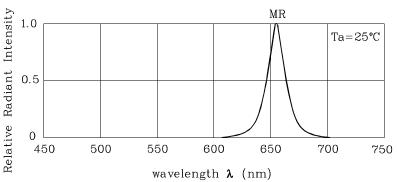
^{*}Luminous intensity value and wavelength are in accordance with CIE127-2007 standards. Oct 06,2014

XDSB8099 V1-X Layout: Maggie L.

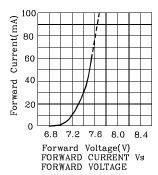
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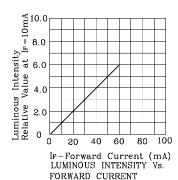
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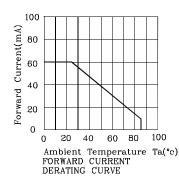
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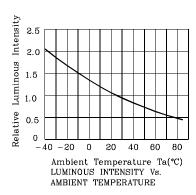


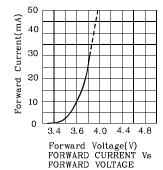
RELATIVE INTENSITY Vs. CIE WAVELENGTH ❖ MR

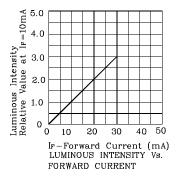


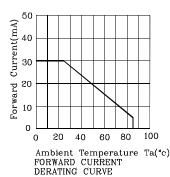


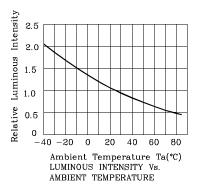




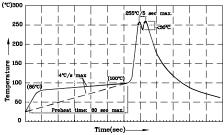








Wave Soldering Profile for Thru-Hole Products (Pb-Free Components)



- Notes:

 1.Recommend pre-heat temperature of 105°C or less (as measured withermocouple attached to the LED pins) prior to immersion in the wave with a maximum solder bath temperature of 280°C

 2.Peak wave soldering temperature between 245°C ~ 255°C for 3 sec
- max).
 3.Do not apply stress to the epoxy resin while the temperature is above 85°C.
 4.Fixtures should not incur stress on the component when mounting and during soldering process.
 5.SAC 305 solder alloy is recommended.
 6.No more than one wave soldering pass.
 7.During wave soldering, the PCB top-surface temperature should be kept below 105°C.

Remarks:

If special sorting is required (e.g. binning based on forward voltage, luminous intensity / luminous flux, or wavelength),

the typical accuracy of the sorting process is as follows:

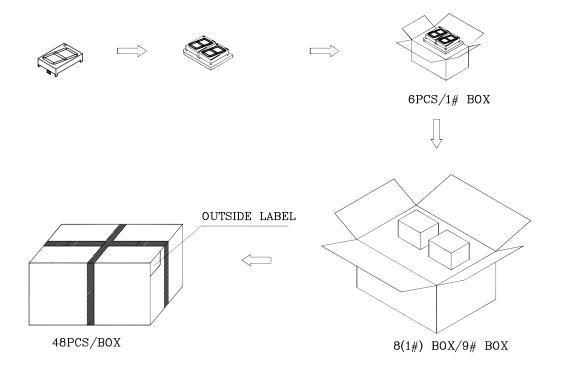
- 1. Wavelength: +/-1nm
- 2. Luminous Intensity / Luminous Flux: +/-15%
- 3. Forward Voltage: +/-0.1V

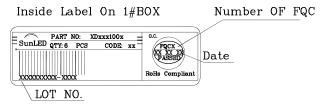
Note: Accuracy may depend on the sorting parameters.



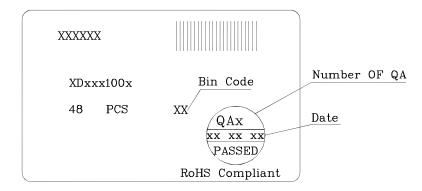
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PACKING & LABEL SPECIFICATIONS





Outside Label On Box



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